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Be Described In VHDL And Implemented On An Altera  
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Ripple-carry Adder Circuit In FigLu-e L. FA FA FA FA S I  
Figure L: A Four-bit Ripple Carry Adder. This Circuit  
Can Be Implemented Using A '+' Sign In VHDL. For  
Example, The Following Code Fragment Adds N-bit Oct  
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